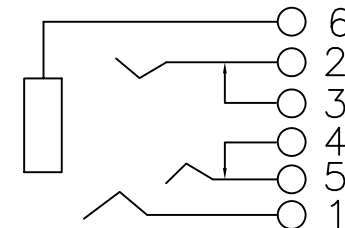
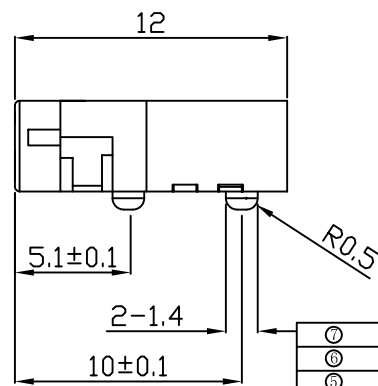
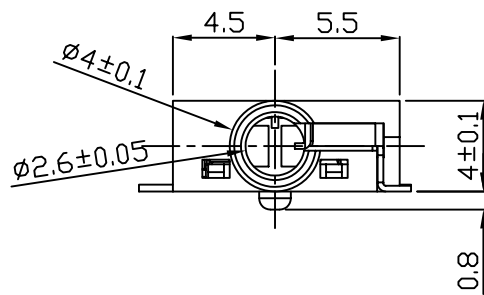


PCB LAYOUT



CIRCUIT DIAGRAM

No.	PART NAME	QTY	MATERIAL (THICK, COLOR)	REMARK
⑦	HOUSING PJ-2543	1	PA (Black)	
⑥	COVER PJ-2543	1	PA (Black)	
⑤	TERMINAL PJ-2543-5#	1	C5210 t=0.25	Au-Plated
④	TERMINAL PJ-2543-4#	2	C2680 t=0.3	Au-Plated
③	TERMINAL PJ-2543-3#	1	C5210 t=0.2	Au-Plated
②	TERMINAL PJ-2543-2#	1	C5210 t=0.2	Au-Plated
①	TERMINAL PJ-2543-1#	1	C5210 t=0.3	Au-Plated

ANGULAR	±5°
16 < L ≤ 63	±0.3
4 < L ≤ 16	±0.2
L ≤ 4	±0.15
UNSPECIFIED TOLERANCE	

DSND	Liyoucai	DATE	2004. 8. 24	SCALE: 3 : 1
CHKD		DATE		VIEW:
APVD	W.Z.S	DATE	2004. 8. 25	UNIT: mm

NAME:	EARPHONE JACK
CAT. NO.	PJ-2543
DWN. NO.	Q/XL-QW-001A-0004

