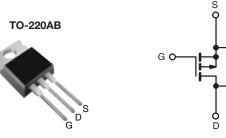


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Power MOSFET

PRODUCT SUMMAI	RY			
V _{DS} (V)	- 1	00		
R _{DS(on)} (Ω)	$V_{GS} = -10 V$	0.60		
Q _g (Max.) (nC)	1	8		
Q _{gs} (nC)	3.0			
Q _{gd} (nC)	9	.0		
Configuration	Sin	gle		



P-Channel MOSFET

FEATURES

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- P-Channel
- 175 °C Operating Temperature
- Fast Switching
- Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC

DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRF9520PbF
Lead (Fb)-liee	SiHF9520-E3
SnPb	IRF9520
	SiHF9520

ABSOLUTE MAXIMUM RATINGS (T_C	= 25 °C, unless otherwis	e noted)			
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	- 100	v	
Gate-Source Voltage		V _{GS}	± 20	v	
Continuous Drain Current	V_{GS} at - 10 V $T_C = 25 \degree C$	- I _D -	- 6.8		
Continuous Drain Current	V_{GS} at - 10 V $T_{C} = 100 ^{\circ}C$		- 4.8	А	
Pulsed Drain Current ^a		I _{DM}	- 27		
Linear Derating Factor			0.40	W/°C	
Single Pulse Avalanche Energy ^b		E _{AS}	300	mJ	
Repetitive Avalanche Current ^a		I _{AR}	- 6.8	А	
Repetitive Avalanche Energy ^a		E _{AR}	6.0	mJ	
Maximum Power Dissipation $T_{C} = 25 \text{ °C}$		PD	60	W	
Peak Diode Recovery dV/dt ^c		dV/dt	- 5.5	V/ns	
perating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	℃	
Soldering Recommendations (Peak Temperature)	for 10 s		300 ^d	U	
Mounting Torque	6-32 or M3 screw		10	lbf ∙ in	
Mounting Torque	0-32 OF INIS SCREW		1.1	N · m	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. $V_{DD} = -25$ V, starting $T_J = 25$ °C, L = 9.7 mH, $R_g = 25 \Omega$, $I_{AS} = -6.8$ A (see fig. 12).

c. $I_{SD} \leq$ - 6.8 A, dI/dt \leq 110 A/µs, $V_{DD} \leq V_{DS}$, $T_J \leq$ 175 °C.

d. 1.6 mm from case.

* Pb containing terminations are not RoHS compliant, exemptions may apply

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THERMAL RESISTANCE RATI	NGS							
PARAMETER	SYMBOL	TYP.		MAX.			UNIT	
Maximum Junction-to-Ambient	R _{thJA}	-		62				
Case-to-Sink, Flat, Greased Surface	R _{thCS}	0.50 -			°C/W			
Maximum Junction-to-Case (Drain)	R _{thJC}	-		2.5				
SPECIFICATIONS (T _J = 25 $^{\circ}$ C, U	Inless otherw	ise noted)						
PARAMETER	SYMBOL	TEST	CONDIT	IONS	MIN.	TYP.	MAX.	UNIT
Static								
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$) V, I _D = -	250 µA	- 100	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	to 25 °C, I	l _D = - 1 mA	-	- 0.10	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V$	' _{GS} , I _D = -	250 µA	- 2.0	-	- 4.0	V
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
		V _{DS} = -	100 V, V _G	_{is} = 0 V	-	-	- 100	
Zero Gate Voltage Drain Current	IDSS	V _{DS} = - 80 V,	V _{DS} = - 80 V, V _{GS} = 0 V, T _J = 150 °C		-	-	- 500	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = - 10 V	I _D	= - 4.1 A ^b	-	-	0.60	Ω
Forward Transconductance	9 _{fs}	V _{DS} =	50 V, I _D =	- 4.1 A ^b	2.0	-	-	S
Dynamic		-						
Input Capacitance	C _{iss}		V _{GS} = 0 V,		-	390	-	
Output Capacitance	C _{oss}	V	$_{\rm DS} = -25$ \	Ι,	-	170	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0 MHz, see fig. 5		-	45	-		
Total Gate Charge	Qg				-	-	18	
Gate-Source Charge	Q _{gs}	V _{GS} = - 10 V		$A, V_{DS} = -80 V,$ iq. 6 and 13 ^b	-	-	3.0	nC
Gate-Drain Charge	Q _{gd}	1	3661	ig. 0 and 10	-	-	9.0	
Turn-On Delay Time	t _{d(on)}				-	9.6	-	
Rise Time	t _r	V _{DD} = -	50 V. In =	- 6.8 A.	-	29	-	
Turn-Off Delay Time	t _{d(off)}	V_{DD} = - 50 V, I _D = - 6.8 A, R _g = 18 Ω, R _D = 7.1 Ω, see fig. 10 ^b		-	21	-	ns	
Fall Time	t _f				-	25	-	
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	nH	
Internal Source Inductance	Ls			-	7.5	-		
Drain-Source Body Diode Characteristic	cs	•						
Continuous Source-Drain Diode Current	١ _S	MOSFET symbol showing the		-	-	- 6.8	А	
Pulsed Diode Forward Current ^a	I _{SM}	integral reverse p - n junction d			-	-	- 27	
Body Diode Voltage	V _{SD}	T _J = 25 °C, I	_S = - 6.8 A	$V_{\rm GS} = 0 \ V^{\rm b}$	-	-	- 6.3	V
Body Diode Reverse Recovery Time	t _{rr}	T 25 °C I	601-11	/dt _ 100 ^ /	-	98	200	ns
Body Diode Reverse Recovery Charge	Q _{rr}	- T _J = 25 °C, I _F =	- 0.0 A, A	ναι = 100 Α/μδ ^ο	-	0.33	0.66	μC
Forward Turn-On Time	t _{on}	Intrinsic turr	n-on time	is negligible (turn	-on is dor	minated b	y L _S and	L _D)

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. Pulse width $\leq 300~\mu s;$ duty cycle $\leq 2~\%.$

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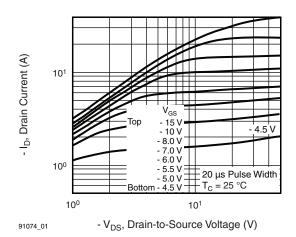


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

V_{GS} - 15 V

- 10 V

- 8.0 V

6.0

5.5

Тор

Bottom

10¹

100

100

- I_D, Drain Current (A)

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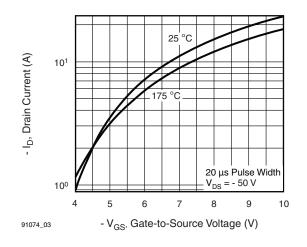


Fig. 3 - Typical Transfer Characteristics

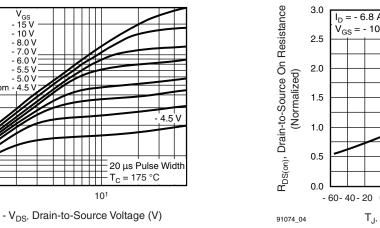


Fig. 2 - Typical Output Characteristics, T_C = 175 °C

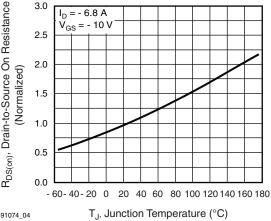


Fig. 4 - Normalized On-Resistance vs. Temperature

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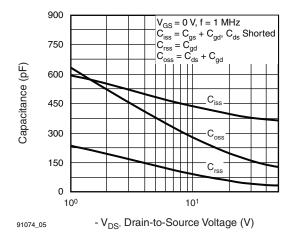
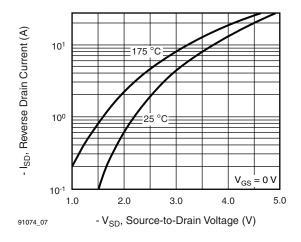


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage





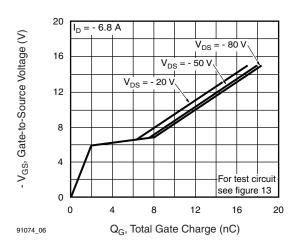


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

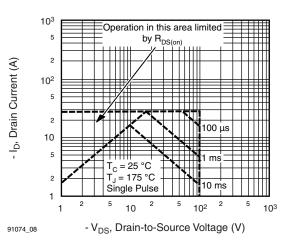


Fig. 8 - Maximum Safe Operating Area

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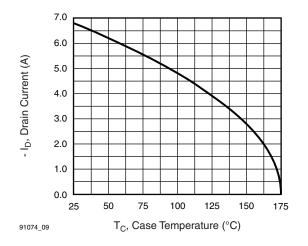


Fig. 9 - Maximum Drain Current vs. Case Temperature

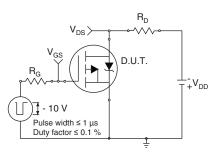


Fig. 10a - Switching Time Test Circuit

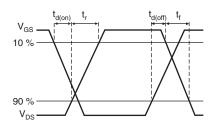


Fig. 10b - Switching Time Waveforms

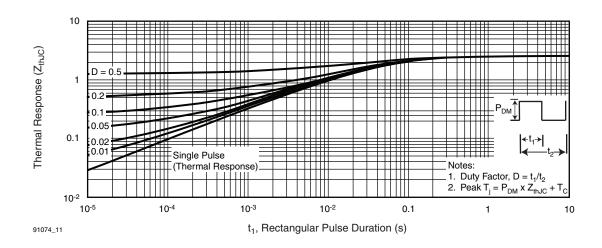


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

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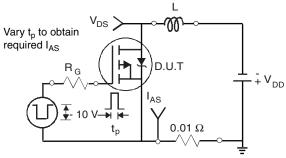


Fig. 12a - Unclamped Inductive Test Circuit

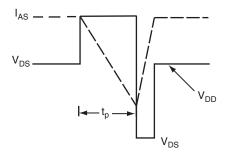


Fig. 12b - Unclamped Inductive Waveforms

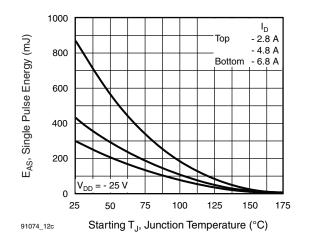


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

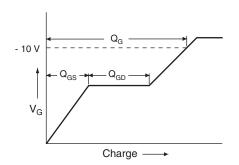


Fig. 13a - Basic Gate Charge Waveform

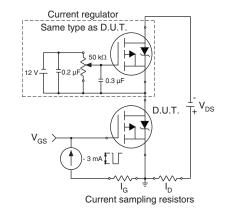
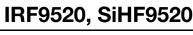


Fig. 13b - Gate Charge Test Circuit

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Peak Diode Recovery dV/dt Test Circuit

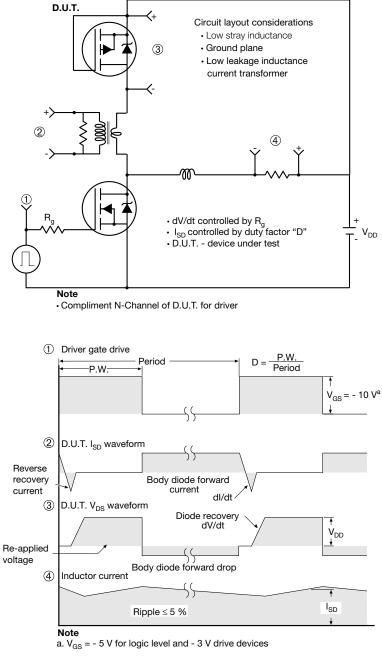


Fig. 14 - For P-Channel

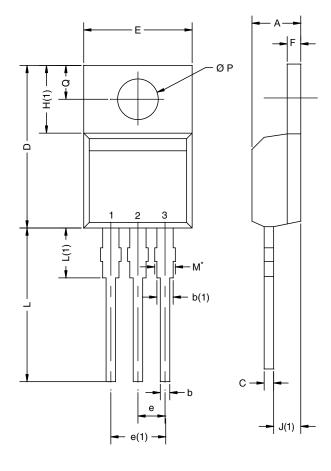
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TO-220AB

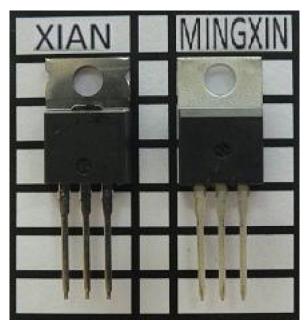


	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØР	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	

Notes

 * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM

Xi'an and Mingxin actual photo



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